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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/072,362	02/07/2002	Kuan-Yu Fu	CPH35726-D1	2716	
7:	590 07/10/2	3			
J.C. Patents			EXAM	EXAMINER	
Suite 250 4 Venture			HU, SHO	UXIANG	
Irvine, CA 92	618		ART UNIT	PAPER NUMBER	
		·	2811		
			DATE MAILED: 07/10/2003	DATE MAILED: 07/10/2003	

Please find below and/or attached an Office communication concerning this application or proceeding.

	•			the
ø\.	, ,	Application No.	Applicant(s)	
•	·	10/072,362	FU, KUAN-YU	
	Office Action Summary	Examiner	Art Unit	
		Shouxiang Hu	2811	
	The MAILING DATE of this communication ap	ppears on the cover sheet wit	h the correspondence address	
Period fo	ORTENED STATUTORY PERIOD FOR REPI	Y IS SET TO EXPIRE 3 MG	ONTH(S) FROM	
THE! - External after - If the - If NO - Failu	MAILING DATE OF THIS COMMUNICATION nsions of time may be available under the provisions of 37 CFR 1 SIX (6) MONTHS from the mailing date of this communication. a period for reply specified above is less than thirty (30) days, a re to period for reply is specified above, the maximum statutory period for reply within the set or extended period for reply will, by stature to reply within the set or extended period for reply will, by staturely received by the Office later than three months after the mailined patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply within the statutory minimum of third will apply and will expire SIX (6) MON to cause the application to become AB	ply be timely filed  (30) days will be considered timely.  (HS from the mailing date of this communication.  ANDONED (35 U.S.C. § 133).	
Status				
1)⊠	Responsive to communication(s) filed on <u>07</u>			
2a) <u></u> □	,	his action is non-final.	to a section on to the morito in	
3) 🗌	Since this application is in condition for allow closed in accordance with the practice under	wance except for formal mai er <i>Ex parte Quayle</i> , 1935 C.I	ters, prosecution as to the ments is ), 11, 453 O.G. 213.	,
Disposit	ion of Claims			
4)⊠	Claim(s) 10-12 is/are pending in the application	tion.		
	4a) Of the above claim(s) is/are withdr	rawn from consideration.		
5)	Claim(s) is/are allowed.			
6)⊠	Claim(s) <u>10-12</u> is/are rejected.			
-	Claim(s) is/are objected to.			
	Claim(s) are subject to restriction and	or election requirement.		
• •	tion Papers			
	The specification is objected to by the Examin		ooted to by the Evaminer	
10)⊠	The drawing(s) filed on <u>07 February 2002</u> is/a	ire: a) accepted or b) ob	ected to by the Examinor.	
445	Applicant may not request that any objection to The proposed drawing correction filed on			
11)[	If approved, corrected drawings are required in		modphioved wy	
12)	The oath or declaration is objected to by the			
-	under 35 U.S.C. §§ 119 and 120			
	Acknowledgment is made of a claim for fore	ian priority under 35 U.S.C.	§ 119(a)-(d) or (f).	
	)⊠ All b)□ Some * c)□ None of:	Э. Р	•	
a,	1.☐ Certified copies of the priority docume	ents have been received.	,	
	2.⊠ Certified copies of the priority docume		Application No. <u>09/059,548</u> .	
	3. Copies of the certified copies of the p			
	application from the International See the attached detailed Office action for a l	Bureau (PCT Rule 17.2(a)). ist of the certified copies not	received.	
	Acknowledgment is made of a claim for dome			on).
15)	<ul> <li>a)                The translation of the foreign language should be a claim for domestic translation and the should be a claim for domestic translation.</li> </ul>	provisional application has t estic priority under 35 U.S.C	een received. . §§ 120 and/or 121.	
Attachme			0 440\ D=== N=(a\	
2) Not	tice of References Cited (PTO-892) tice of Draftsperson's Patent Drawing Review (PTO-948) ormation Disclosure Statement(s) (PTO-1449) Paper No(s	5) Notice of	Summary (PTO-413) Paper No(s) Informal Patent Application (PTO-152)	
U.S. Patent and	d Trademark Office	Action Summary	Part of Paper No. 3	

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#### **DETAILED ACTION**

#### **Drawings**

1. The drawings are objected to because Fig 2C contains informal handwritings. A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

### Claim Objections

2. Claims 10-12 are objected to because of the following informalities and/or defects:

In claim 10, the terms of "first deep source/drain junction region" and "second source/drain junction region" should read respectively as: --deep source region-- and -- deep drain region--, to better reflect the subject matters of the instant invention that the real effective source or drain junction (formed with the channel region) is located beneath the trench, as shown in Fig. 4D of the instant disclosure.

In addition, the term of "trench disposed in the channel region" recited in claim 10 should read as: --trench disposed near the channel region—, since the real channel region is located outside the trench, as shown in Fig. 4D of the instant invention. And, the term of "disposed over the said" recited in claim 10 should read as: --disposed over said--.

Appropriate correction is required.

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### Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 4. Claims 10 and 12, as being best understood in view of the claim objections set forth above, are rejected under 35 U.S.C. 102(b) as being anticipated by Lancaster (US 4,835,584).

Lancaster discloses a semiconductor structure (Figs. 1-5L), comprising a substrate having an active region including a channel region (under each bottom of the gate oxide layer 57) and a non-channel region surrounding the channel region; a first trench and a second trench disposed near the channel region, a thick insulating layer (57; a silicon oxide gate insulating layer, about 0.1 um, see col. 3, line 61) over the first and second trenches and conformal to the profile of the first and second trenches; a gate electrode (58) disposed over the two trenches and comprising a first vertical portion, a second vertical portion and a horizontal portion, with the first vertical portion being embedded inside the first trench, the second vertical portion being embedded inside the second trench, and the horizontal portion being disposed over the substrate and connecting the first and second vertical portions together; a first shallow doped region (the top horizontal portion of region 73) within the substrate at an upper corner adjacent to the first vertical portion of region 74) at an upper corner adjacent to the

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second vertical portion of the gate electrode; a deep source region (the lower portion of region 73) and a deep drain region (the lower portion of region 74) disposed in a region in the substrate at a depth deeper than the first and second trenches, wherein the active region has a first conductivity type, as the substrate it self has first-type impurities therein (see col. 7, lines 42-43); and source/drain junction naturally exists between the source/drain and the channel region.

#### Claim Rejections - 35 USC § 103

- 5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 6. Claim 11 is rejected under 35 U.S.C. 103(a) as being unpatentable over Lancaster (US 4,835,584) in view of Kimura (US 5029,321).

The disclosure of Lancaster is discussed as applied to claims 10 and 12.

Although Lancaster does not expressly disclose that the thick insulating layer is formed through thermal oxidation, one of ordinary skill in the art would readily recognize that such thick insulating layer used as a gate oxide layer can be readily formed through thermal oxidation for achieving high quality in the gate insulating layer, as evidenced in Kimura (see the thermal oxide layer 3 in Fig. 7E).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to make the semiconductor structure of Lancaster with the

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thick insulating layer (the gate oxide layer) being formed through thermal oxidation, as taught in Kimura, so that a semiconductor device with a high quality gate insulting layer would be obtained.

## Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. References C and D are cited as being related to a semiconductor device having a trenched gate structure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Shouxiang Hu whose telephone number is (703) 306-5729. The examiner can normally be reached on Monday through Thursday, 7:30 AM to 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (703) 308-2772. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9318 for regular communications and (703) 872-9319 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

SH June 27, 2003

Shouxiang Hu
Patent Examiner

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